

DATA PATHS WITH RECEIVER TIMING FIXABLE TO A DOWNSTREAM STAGE AND METHODS OF OPERATION THEREOF

ABSTRACT OF THE DISCLOSURE

A data path includes a downstream stage that strobes data at an input thereof responsive to a first control signal, an upstream stage that sends data to the input of the downstream stage responsive to a second control signal, and a control circuit operative to fix timing of the second control signal to timing of the first control signal.

- 5 The data path may further include a second upstream stage that sends data to an input of the first upstream stage responsive to a third control signal having a timing with respect to the second control signal that varies responsive to a frequency at which data is transferred along the data path. A fixed delay circuit, e.g., a fixed delay circuit in a forward path of a DLL or PLL, may generate the first control signal from the second
- 10 control signal.